



## CHALLENGE #21

### UOFR-SEC-01

**Making integrated circuits more secure against various security threats while maintaining high power efficiency and low area**

Meet the expectations of this US Node through the technology challenge described below



### GOALS

Determine an existing mechanism/circuit/algorithm with a primary objective of improving the power efficiency/performance or reducing area and leverage them for security with small modifications.

### DETAILS

The security mechanisms implemented to protect integrated circuits (ICs) from malicious attacks typically result in significant power, performance, and area overhead. Some of the existing components in an IC can be leveraged in a security aware fashion to minimize the related overheads while also making the ICs more secure.